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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent of

**Certificate
OCT 26 2004
of Correction**

U.S. Patent No. : 6,767,784 B2
Issued : July 27, 2004
Title : **LATCH-UP PREVENTION FOR MEMORY CELLS**
Docket No. : MIO 0042 V2/40509.141

Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on October 19, 2004.


James E. Beyer Reg. No. 39,564

Sir:

REQUEST FOR CERTIFICATE OF CORRECTION FOR PTO MISTAKE
UNDER 35 U.S.C. 254 and 37 C.F.R. 1.322

In the matter of U.S. Patent No. 6,767,784 B2, it is respectfully requested that a Certificate of Correction be issued to cover the following errors which occurred in the printing of the patent.

- Col. 2, line 2, "bipolar transistor and an" should read --bipolar transistor 20 and an--;
- Col. 11, line 58, "first voltage input V_{CC}" should read -- first voltage input V_{CC}--;
- Col. 12, line 42, "layer first" should read -- layer/first--;
- Col. 13, Claim 1, line 67, "conductivity," should read --conductivity;--; and
- Col. 13, Claim 2, line 25, "fanning" should read --forming--.

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Patent No. 6,767,784 B2
Docket No. MIO 0042 V2/40509.141

REMARKS

It is respectfully requested that a Certificate of Correction be issued for the above-identified patent. All of the above errors occurred during the printing of the patent and, therefore, no fee is due. Two (2) copies of the required Certificate of Correction Form PTO/SB/44 are enclosed.

Respectfully submitted,
DINSMORE & SHOHL LLP

By



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JEB/vlh
Encl.

OCT 26 2004

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,767,784 B2
DATED : July 27, 2004
INVENTOR(S) : Porter et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 2, line 2, "bipolar transistor and an" should read --bipolar transistor 20 and an--;
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MAILING ADDRESS OF SENDER:

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PATENT NO. 6,747,784 B2

No. of additional copies



1 of 1

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

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